



DESCRIPTION

PT2353H is a 3-speaker system electronic crossover IC utilizing CMOS Technology specially designed for the new generation of audio equipments. 3-channel electronic volume control, 2-channel 2nd order high pass filter, 2nd order low pass filter and one mono subwoofer audio output are incorporated into a single high performance chips. PT2353H combines the electronic volume controller and crossover circuitry without MCU handling. Pin assignments and application circuit are optimized for easy PCB layout and cost saving advantages.

FEATURES

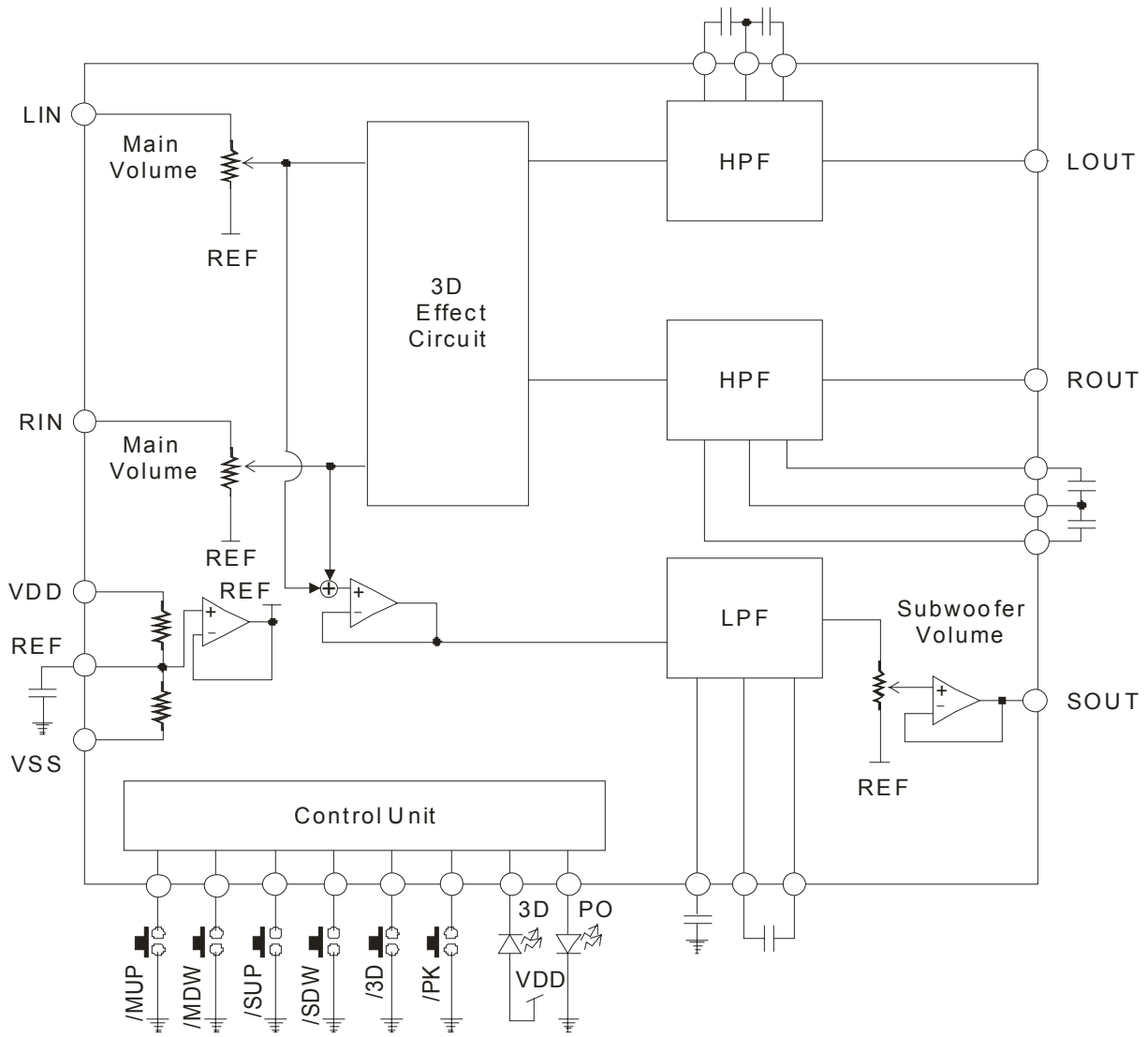
- CMOS Technology
- Low Power Consumption
- 2 Built-in Individual Volume Control: 0 dB to -60dB
- 2-Channel 2nd Order High Pass Filter + 1 Mixed Mono Subwoofer Low Pass Filter
- Cross-Over Frequency adjustable by external passive components
- Control Interface of Volume Control: UP/DOWN Key (MCU is not necessary)
- Built-in 3D Effect and Loudness Function
- Supply Voltage: 5V to 9V

APPLICATION

- Audio Equipments

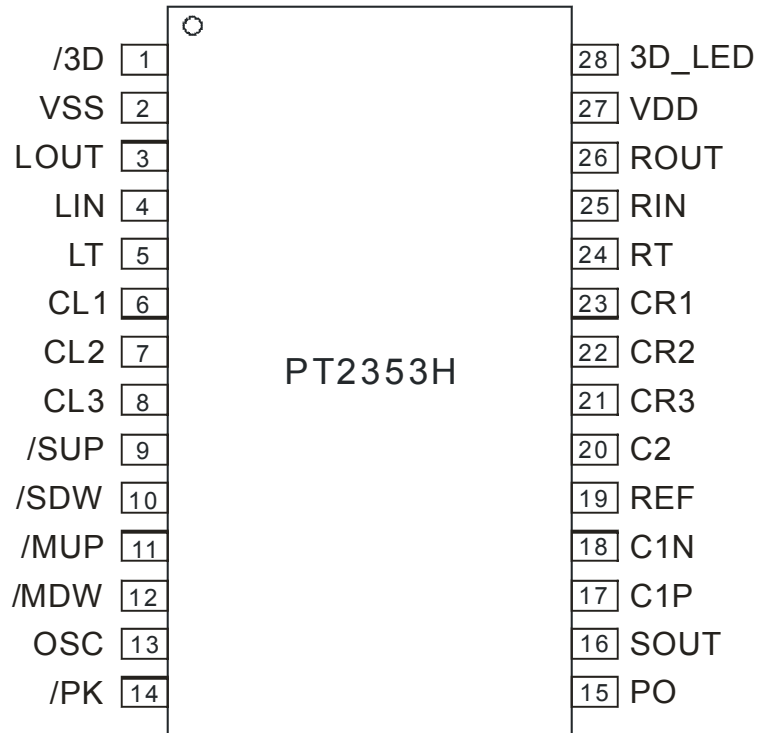


BLOCK DIAGRAM





PIN CONFIGURATION





PIN CONFIGURATION

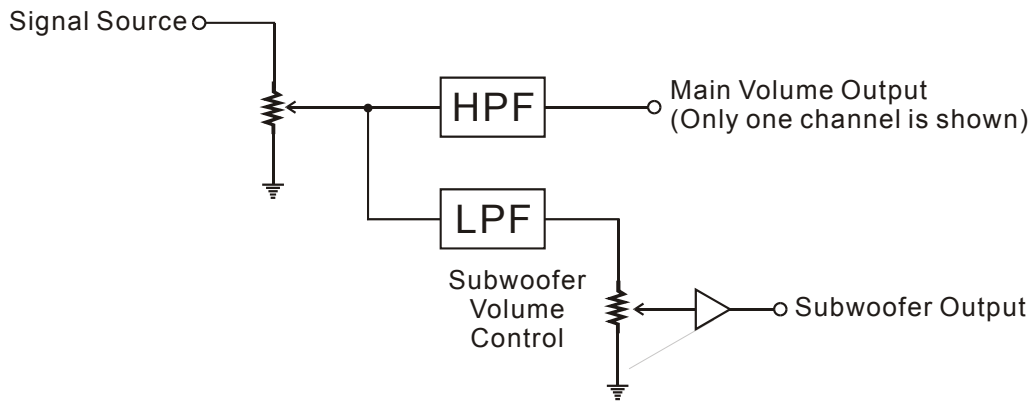
Pin Name	I/O	Description	Pin No.
/3D	I	3D Effect Enable Pin (Low: Active)	1
VSS	-	Ground Pin	2
LOUT	O	Left Channel Output Pin	3
LIN	I	Left Channel Input Pin	4
LT	I	Left Channel Center Tap (Loudness) Pin	5
CL1	I	Left Channel High Pass Filter Capacitor Pin 1	6
CL2	I	Left Channel High Pass Filter Capacitor Pin 2	7
CL3	I	Left Channel High Pass Filter Capacitor Pin 3	8
/SUP	I	Subwoofer Channel Volume Up Control Pin (Low: Active)	9
/SDW	I	Subwoofer Channel Volume Down Control Pin (Low: Active)	10
/MUP	I	Main Volume Up Control Pin (Low: Active)	11
/MDW	I	Main Volume Down Control Pin (Low: Active)	12
OSC	I	Oscillation Pin (connects to external RC components)	13
/PK	I	PO Control Pin (Low: Active)	14
PO	O	Power Amplifier Control Pin (controlled by PK pin 14)	15
SOUT	O	Subwoofer Channel Output Pin	16
C1P	I	Low Pass Filter External Capacitor Pin	17
C1N	I	Low Pass Filter External Capacitor Pin	18
REF	I	Reference Voltage = $\frac{1}{2}$ VDD	19
C2	I	Low Pass Filter External Capacitor Pin 2	20
CR3	I	Right Channel High Pass Filter Capacitor Pin 3	21
CR2	I	Right Channel High Pass Filter Capacitor Pin 2	22
CR1	I	Right Channel High Pass Filter Capacitor Pin 1	23
RT	I	Right Channel Volume Center Tap (Loudness) Pin	24
RIN	I	Right Channel Input Pin	25
ROUT	O	Right Channel Output Pin	26
VDD	-	Positive Power Supply	27
3D-LED	O	3D LED Indicator Pin	28



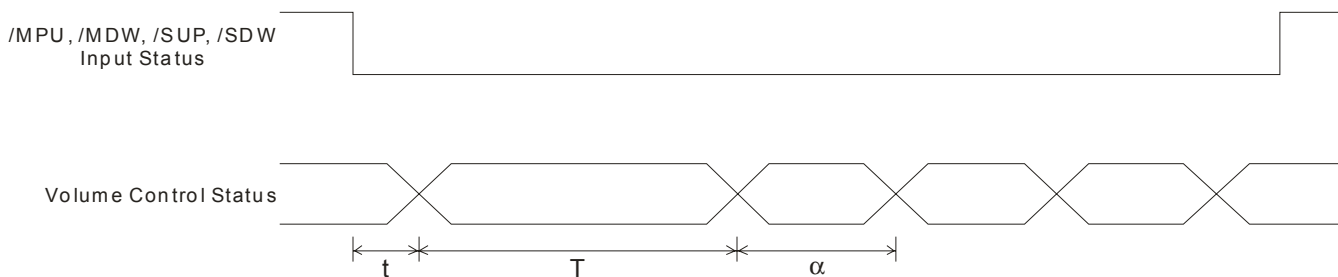
FUNCTION DESCRIPTION

VOLUME CONTROL

Both main volume control and subwoofer volume control are supported by PT2353H. Please refer to the diagram below.



The main volume control is used to adjust all audio volume levels. The subwoofer volume control can be adjusted to match the main volume control. The Main Volume Control is determined by the MUP and MDW pins. If the MUP is connected to the Ground, the main volume level output may be increased. If the MDW is connected to the Ground, the main volume level output can be decreased. The Subwoofer Volume Control is determined by the SUP and SDW pins. If the SUP is connected to Ground, the subwoofer volume level output can be increased. If the SDW is connected to the Ground, the subwoofer volume level output can be decreased. When each of the MUP, MDW, SUP, SDW pins are connected to the GND for an indefinite period of time, please refer to the following diagram.



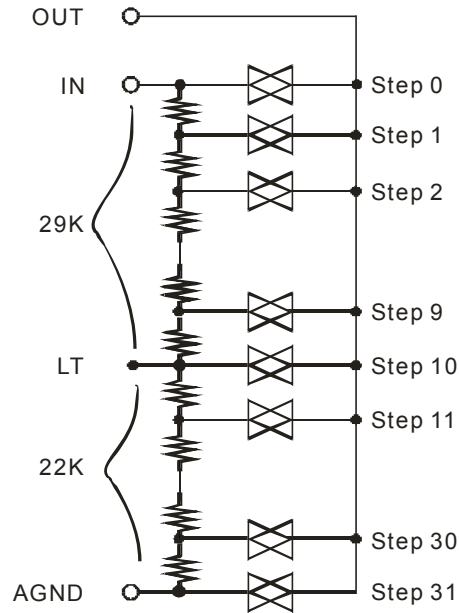
Notes:

1. t =Default Key Time
2. T =Hold Time ($10 \times 1/f_{osc}$)
3. α =Volume UP/DOWN Switch Time ($2 \times 1/f_{osc}$)
4. f_{osc} =Internal Oscillation Frequency



VOLUME ATTENUATION

The equivalent circuit is shown below.



A PT2353H volume control circuit consists of ladder resistors and analog switches. The Tap for Loudness Function (LT & RT) is connected to the Step 10 (-20dB). Therefore, the Loudness Function Effect will be clearly audible when the volume setting is less than -20dB. Please refer to the attenuation table given below.

Step	Attenuation	Step	Attenuation
0	0	16	-32
1	-2	17	-34
2	-4	18	-36
3	-6	19	-38
4	-8	20	-40
5	-10	21	-42
6	-12	22	-44
7	-14	23	-46
8	-16	24	-48
9	-18	25	-50
10	-20	26	-52
11	-22	27	-54
12	-24	28	-56
13	-26	29	-58
14	-28	30	-60
15	-30	31	∞

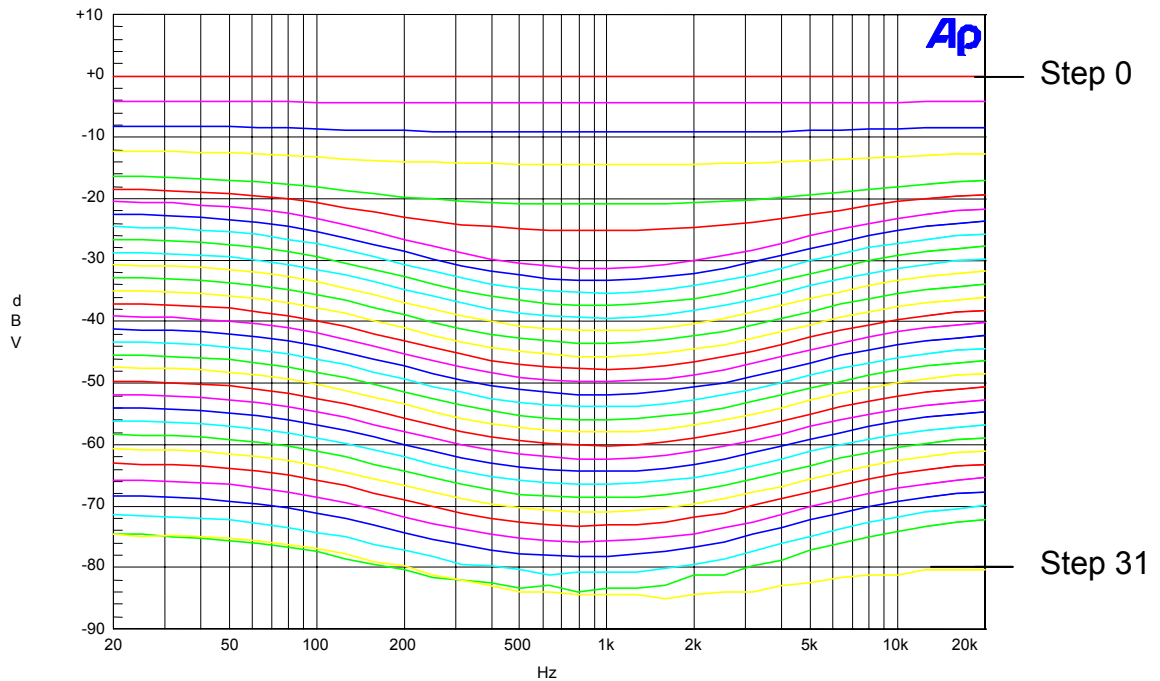


VOLUME INITIALIZATION

After power is turned ON, the volume control value of the main channel is set to the initial setting of -30dB step while the subwoofer channel will be set at 0dB.

LOUDNESS FREQUENCY RESPONSE CURVE

The PT2353H loudness frequency response curve from Step 0 to Step 31 is given below.



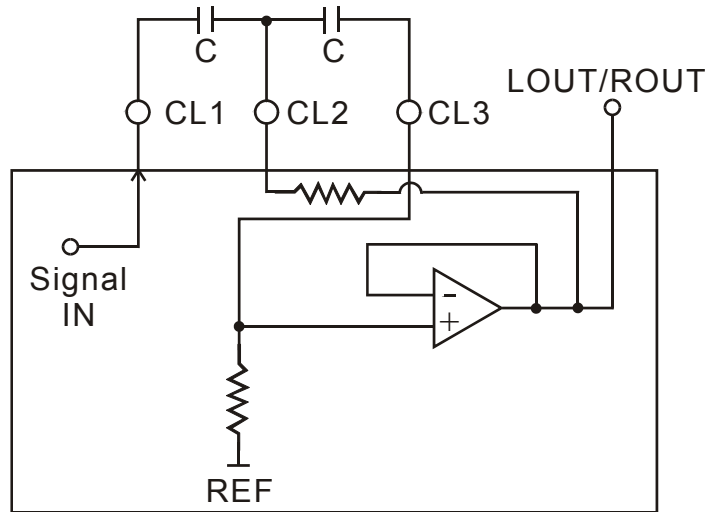


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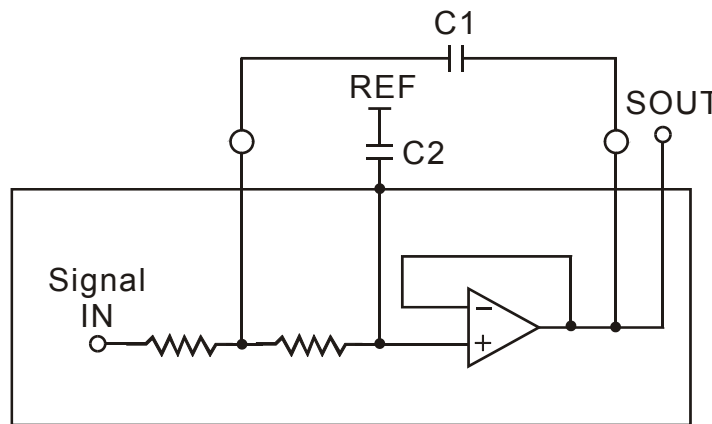
PT2353H

FILTER

The main channel of PT2353H includes the high pass filter which is used to filter out the main speaker's low frequency characteristics. The filtered signal is then combined with the subwoofer low pass filter characteristics. This High Pass Filter is of Sallen Key Type with a pass band gain equivalent to 0dB. Adjustment of the crossover frequency is made via the two external capacitors. Please refer to the figure below.



Subwoofer channel mixed from the Main Channel also uses the Sallen Key Type Low Pass Filter to filter out the high frequency characteristics. Two external capacitors can be used to adjust the crossover frequency. Please refer to the diagram below.



The subwoofer in this 3-speaker system has only one channel output; therefore, it is recommended that the LPF must be less than 300Hz. If the frequency is greater than 300Hz, then the audio output can be clearly discernible. That is, it is easy to know where the sound is coming from. The following diagram provides the suggested crossover frequencies and respective external capacitors.



3D EFFECT FUNCTION

The 3D pin controls PT2353H's 3D effect function. It toggles between turning the 3D effect function ON and OFF. When the 3D effect function is enabled, the 3D LED is displayed. When the PO pin in low state the 3D function will backup and both 3D effect and 3D LED are inhibited.

POWER CONTROL

PK (Pin 14) is the PO Control Pin and is used to toggle between activating and deactivating the PO pin (Pin 15). The power amplifier MUTE or Standby status (ON or OFF) may be controlled by connecting the external power amplifier device to the PO Pin.

POWER ON INITIALIZATION

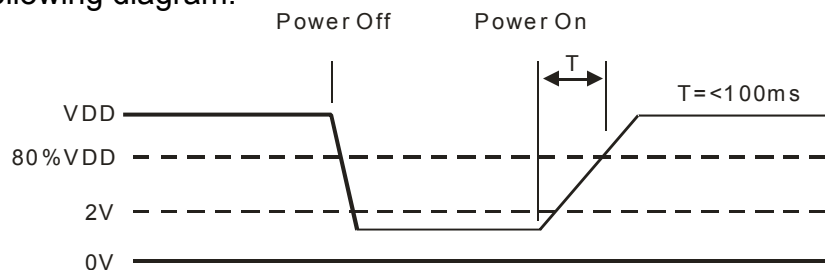
After power is first applied to PT2353H's VDD pin:

- the output state for the PO pin is set to HIGH.
- the 3D function is set to OFF.
- the Master Volume is set to -30dB and Subwoofer Volume is set to 0dB.
- all the function keys are enabled.

When the Power is reset:

- the VDD must drop below 2V.
- the Power supply rising time must be below 100mS, otherwise the Power Reset function will fail.

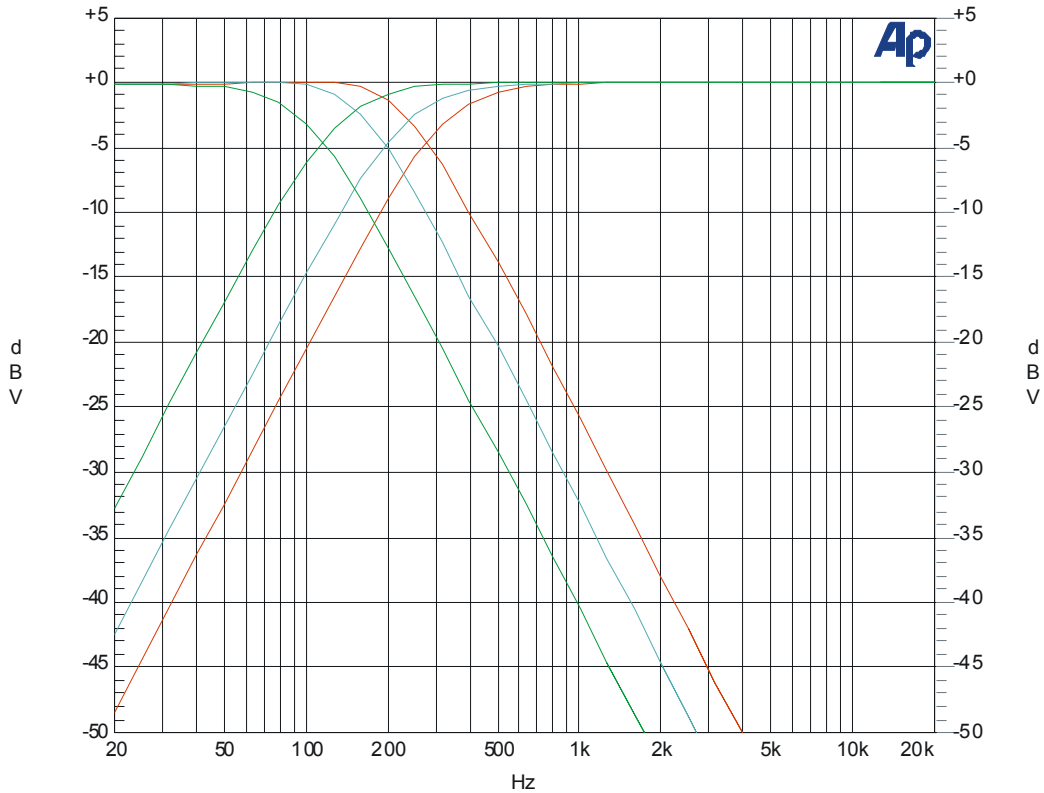
Please refer to the following diagram.





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Notes:

- Crossover Frequency: 280Hz, C=0.015 μ f, C1=0.047 μ f, C2=0.018 μ f
- - - Crossover Frequency: 200Hz, C=0.022 μ f, C1=0.068 μ f, C2=0.027 μ f
- — Crossover Frequency: 120Hz, C=0.033 μ f, C1=0.1 μ f, C2=0.047 μ f

C is the High Pass Filter External Capacitor.
C1 and C2 are the Low Pass Filter External Capacitors



ABSOLUTE MAXIMUM RATING

Parameter	Symbol	Rating	Unit
Operating Voltage	Vs	10.2	V
Operating Temperature	Topr	-40 to +85	°C
Storage Temperature	Tstg	-65 to +150	°C

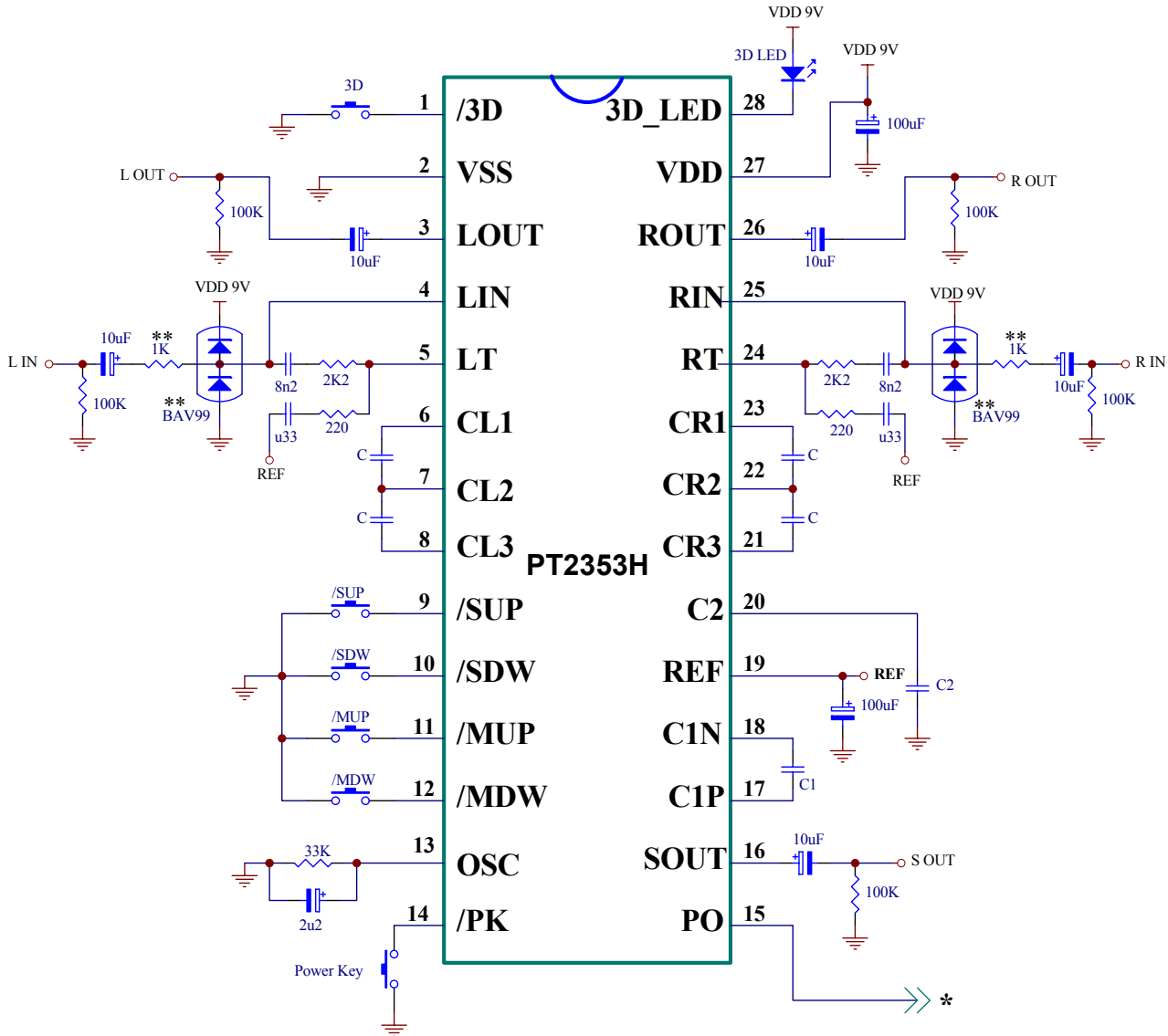
ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, Ta=25°C, VDD=9V, RL=100KΩ, f=1KHz)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply	VDD		5	-	9.5	V
Operating Current	Is	3D, PK OFF, VDD=9V 3D, PK OFF, VDD=4.5V	-	23 18	-	mA
Total Harmonic Distortion	THD	ATT=0dB, F=1KHz VIN=1Vrms, 20Hz ~ 20KHz All filters bypass, All outputs	-	0.01	-	%
		ATT=-20dB, F=1KHz VIN=1Vrms, 20Hz ~ 20KHz All filters bypass, All outputs	-	0.05	-	
Output Noise	NO	ATT=0dB, VIN=0V A-weighted, All filters bypass	-	15	-	μV
Signal-to-Noise Ratio	S/N	0dB=1Vrms, ATT=0dB VIN=0V, A-weighted All filters bypass	-	90	-	dB
Maximum Input Voltage	VIMAX	f=1KHz, ATT=0dB VDD=9V	-	2.7	-	Vrms
Voltage Gain	Gn	f=1KHz, ATT=0dB All Outputs	-1	0	+1	dB
Cross Talk	CT	0dB=1Vrms, f=1KHz VIN=1Vrms, RL=100K RG=600Ω, A-weighted	-	-86	-	dB
Maximum Volume Attenuation	ATTMAX	f=1KHz, VIN=1Vrms LOUT, ROUT	-	-75	-	dB
		Main Volume =Max, SOUT	-	-75	-	dB
High Level Input Voltage	VIH		6.7	-	9	V
Low Level Input Voltage	VIL		0	-	3.2	V
LED Sink Current	IS	3D LED Pin	-	6	-	mA
Drive Current	IO	PO	-	5	-	mA
Output Impedance	RO	Vout=100mV f=1KHz	70	100	130	Ω
Input Impedance	RI		40	50	60	KΩ



APPLICATION CIRCUIT



Notes:

1. Please refer to the PT2353H Crossover Frequency Response Curve for the values of C, C1, and C2.
2. Please refer to Power Control (PO) Section of this document.
3. ** For input port high voltage pulse protection.



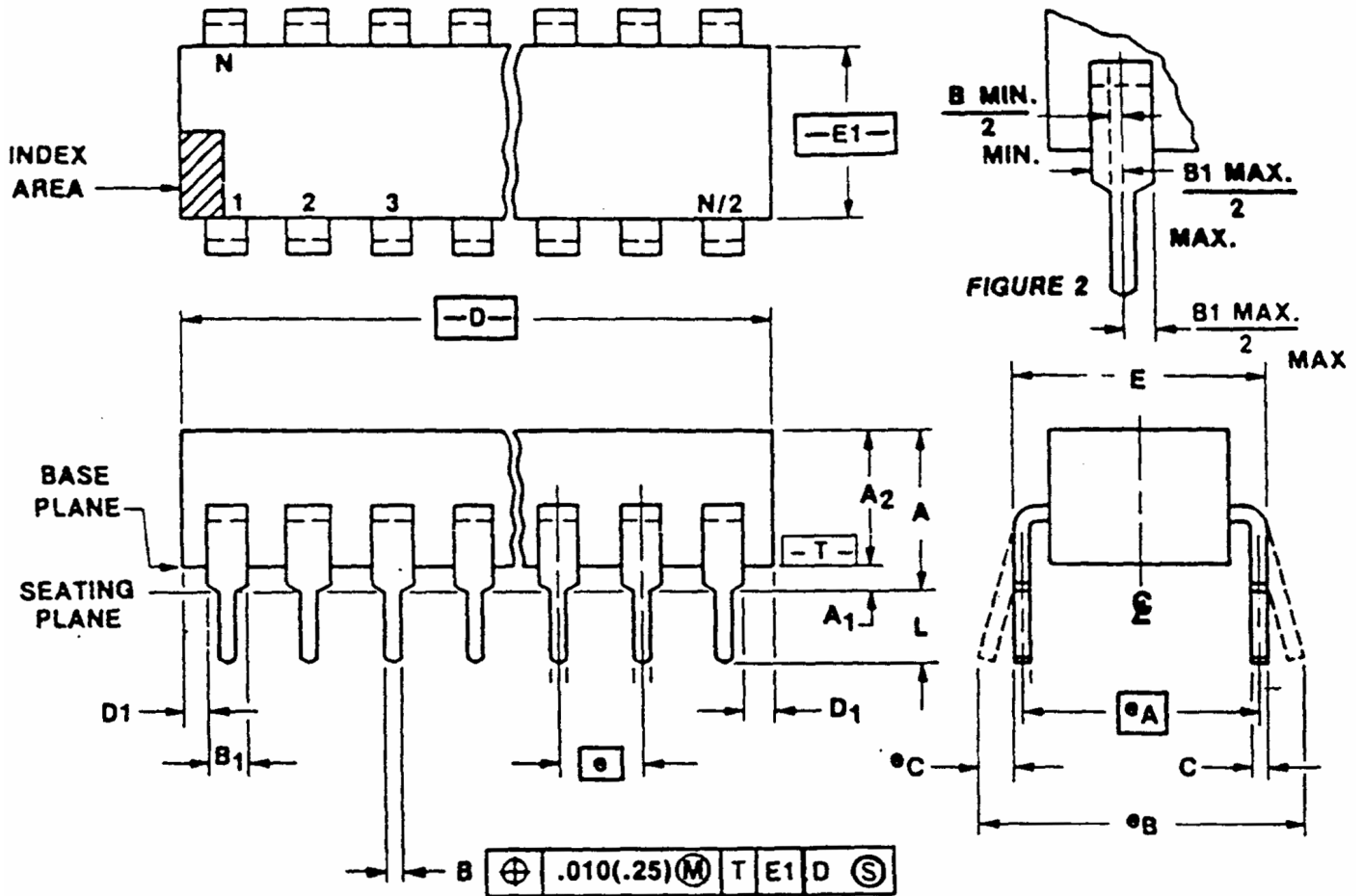
ORDER INFORMATION

Valid Part Number	Package Type	Top Code
PT2353H	28 Pins, DIP, 600mil	PT2353H
PT2353H-S	28 Pins, SOP, 300mil	PT2353H-S



PACKAGE INFORMATION

28 PINS, DIP, 600MIL





Symbol	Min.	Max.
A	-	0.25
A1	0.015	-
A2	0.125	0.195
B	0.014	0.022
B1	0.030	0.070
C	0.008	0.015
D	1.38	1.565
D1	0.005	-
E	0.60	0.625
E1	0.485	0.58
e	.10 BASIC	
eA	.60 BASIC	
eB	-	0.70
L	0.115	0.20

Notes:

1. All dimensions are in INCHES.
2. Dimensioning and tolerancing per ANSI Y14.5M-1982.
3. Dimensions A, A1 and L are measured with the package seated in JEDEC Seating Plane Gauge GS-3.
4. "D" & "E1" dimensions, for ceramic packages, include allowance for glass overrun and meniscus and lid to base mismatch.
5. "D" & "E1" dimensions for plastic package, do not includes mold flash or protrusion. Mold flash or protrusions shall not exceed 0.01 inch. (0.25mm).
6. "E" and "eA" measured with the leads constrained to be perpendicular to plane T.
7. "eB" and "eC" are measured at the lead tips with the loads un-constrained. "eC" must be zero or greater.
8. "N" is the maximum quantity of lead positions. (N=28)
9. Corner leads (1, N, N/2, and N/2+1) may be configured as shown in Figure 2.
10. Pointed our rounded leads tips are preferred to ease insertion.
11. For automatic insertion, any rained irregularity on the top surface (step, mess, etc.) shall be symmetrical about the lateral and longitudinal package centerlines.
12. Refer JEDEC MS-011 Variation AB.

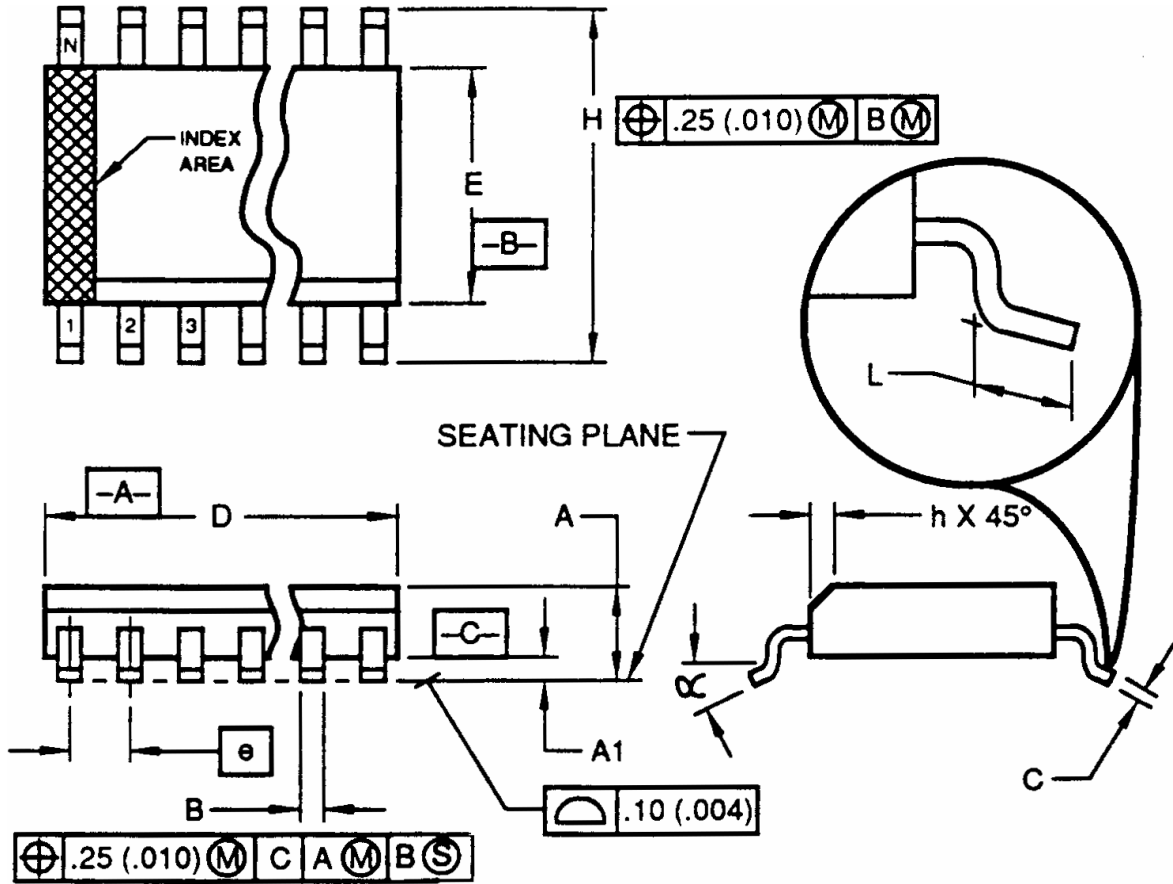
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PT2353H

28 PINS, SOP, 300MIL



Symbol	Min.	Nom.	Max.
A	2.35		2.65
A1	0.10		0.30
B	0.33		0.51
C	0.23		0.32
D	17.70		18.10
E	7.40		7.60
e		1.27 BSC.	
H	10.00		10.65
h	0.25		0.75
L	0.40		1.27
α	0°		8°



Satellite + Subwoofer 3-Speaker System Electronics Crossover IC

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Notes:

1. Refer to applicable symbol list.
2. Dimensioning and tolerancing per ANSI Y 14.5M-1982.
3. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusions and gate burrs shall not exceed 0.15mm (0.06 in) per side.
4. Dimension "E" does not include inter-lead flash or protrusions. Inter-lead flash and protrusions shall not exceed 0.25mm (0.10 in) per side.
5. The chamfer on the body is optional. If it is not present, a visual index feature must be located within the crosshatched area.
6. "L" is the length of terminal for soldering to a substrate.
7. "N" is the number of terminal positions. (N=28)
8. Terminal numbers are shown for reference only.
9. The lead width "B" as measured 0.36mm (0.14 in) or greater above the seating plane, shall not exceed a maximum value of 0.61mm (0.24 in).
10. Controlling dimension: MILLIMETER
11. Refer to JEDEC MS-013 Variation AE.

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