

QUAD OPERATIONAL AMPLIFIERS

DESCRIPTION

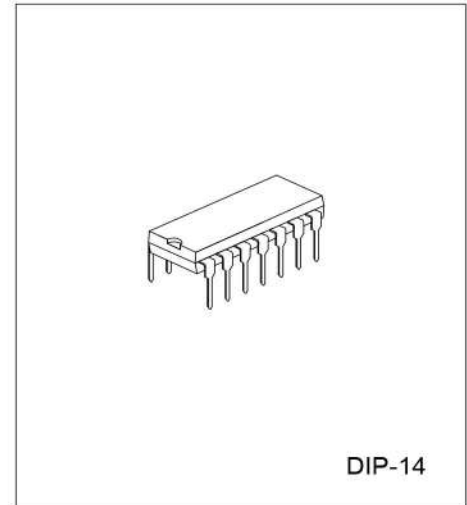
The LM324N consists of four independent, high gain internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide voltage range.

Operation from split power supplies is also possible so long as the difference between the two supplies is 3 Volts to 32 volts.

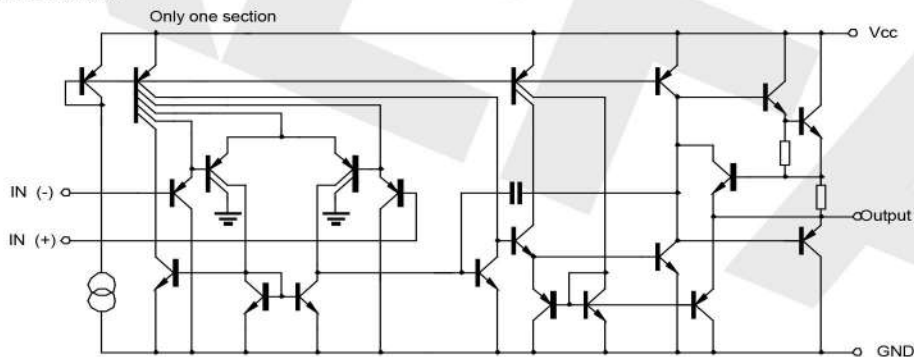
Application areas include transducer amplifier, DC gain blocks and all the conventional OP amp circuits which now can be easily implemented in single power supply system.

FEATURES

- *Internally frequency compensated for unity gain
- *Large DC voltage gain : 100dB
- *Wide operating supply range ($V_{cc}=3V\sim 32V$)
- *Input common-mode voltage includes ground
- *Large output voltage swing: From 0V to $V_{cc}-1.5V$
- *Power drain suitable for battery operation



BLOCK DIAGRAM

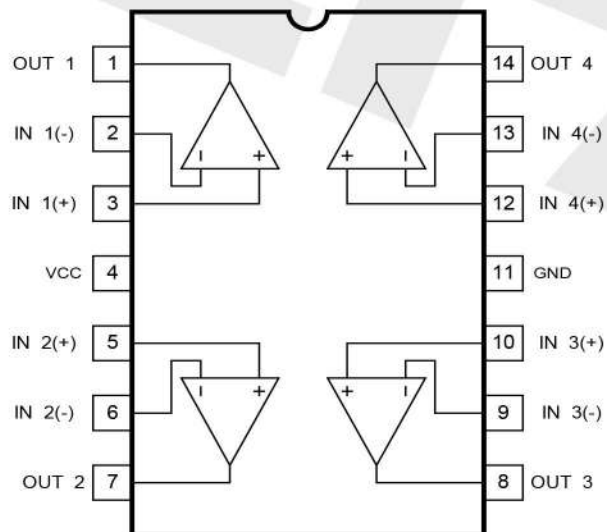


ABSOLUTE MAXIMUM RATINGS ($T_a=25^{\circ}C$)

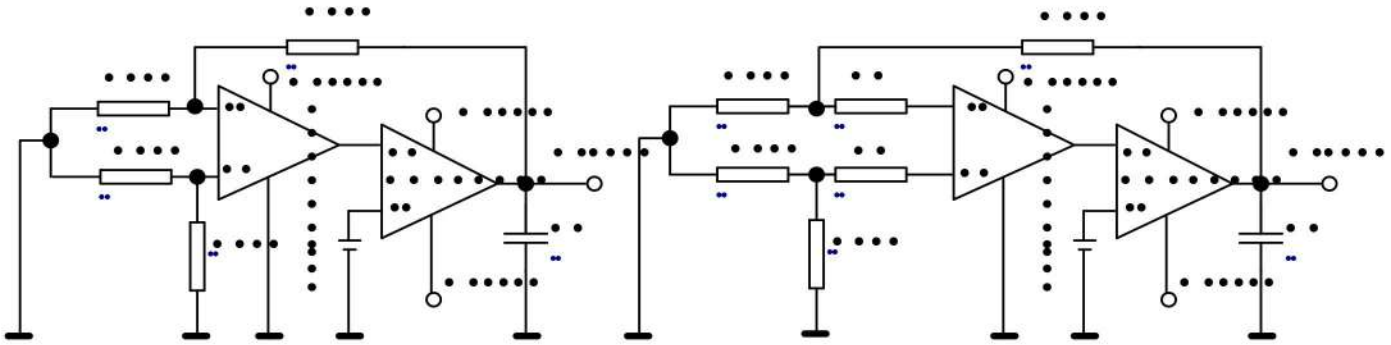
Characteristic	Symbol	Value	Unit
Supply Voltage	V_{cc}	32	V
Differential input voltage	$V_{i(diff)}$	32	V
Input Voltage	V_I	-0.3~32V	V
Power Dissipation	P_d	720	mW
Operating Temperature	T_{opr}	-30 to +85	$^{\circ}C$
Storage Temperature	T_{stg}	-55 to +125	$^{\circ}C$

ELECTRICAL CHARACTERISTICS ($T_a=25^{\circ}\text{C}$)($V_{CC}=5.0\text{V}$, All voltage referenced to GND unless otherwise specified)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Input offset voltage	V_{IO}	$V_{CM}=0$ to $V_{CC}-1.5$ $V_{O(p)}=1.4\text{V}, R_s=0$		± 2	± 7	mV
Input offset current	I_{IO}			± 5	± 50	nA
Input Bias current	I_b			45	250	nA
Input Common-mode voltage range	$V_{I(R)}$	$V_{CC}=30\text{V}$	0		$V_{CC}-1.5$	V
Supply Current	I_{CC}	$R_L=\infty, V_{CC}=30\text{V}$		1.5	3	mA
		$V_{CC}=5\text{V}$		0.6	2	mA
Large signal Voltage Gain	G_V	$V_{CC}=15\text{V}, R_L>2\text{k}\Omega$ $V_{O(p)}=1\text{V}$ to 11V	25	100		V/mV
Output voltage Swing	V_O		0	-	$V_{CC}-1.5$	V
Common-mode rejection Ratio	CMRR		65	80		dB
Power supply rejection Ratio	PSRR		65	100		dB
Channel Separation	CS	$f=1\text{kHz}$ to 20kHz		120		dB
Short circuit to GND	I_{SC}			40	60	mA
Output current	I_{SOURCE}	$V_I(+)=1\text{V}, V_I(-)=0$ $V_{CC}=15\text{V}, V_{O(p)}=2\text{V}$	20	40		mA
	I_{SINK}	$V_I(+)=0\text{V}, V_I(-)=1\text{V}$ $V_{CC}=15\text{V}, V_{O(p)}=2\text{V}$	10	20		mA
Differential input voltage	$V_I(\text{diff})$				V_{CC}	V

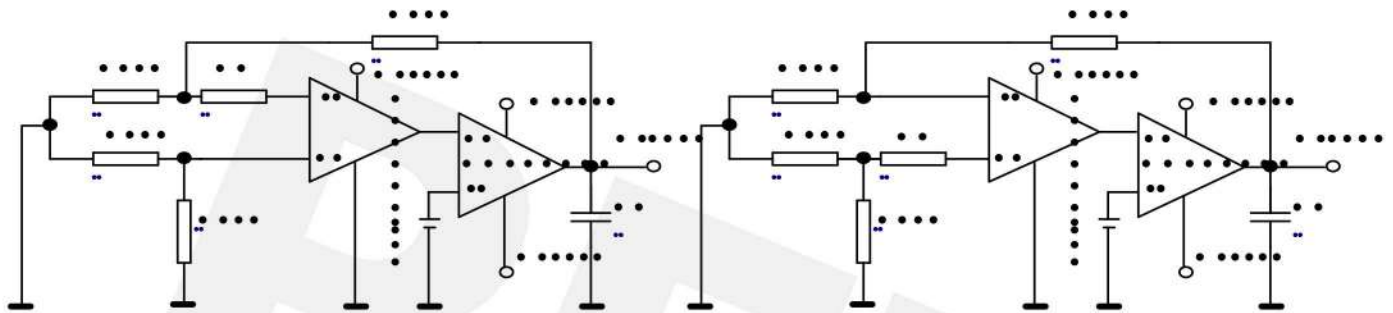
PIN CONFIGURATION

Test Circuit (Note: NULL Amplifier)

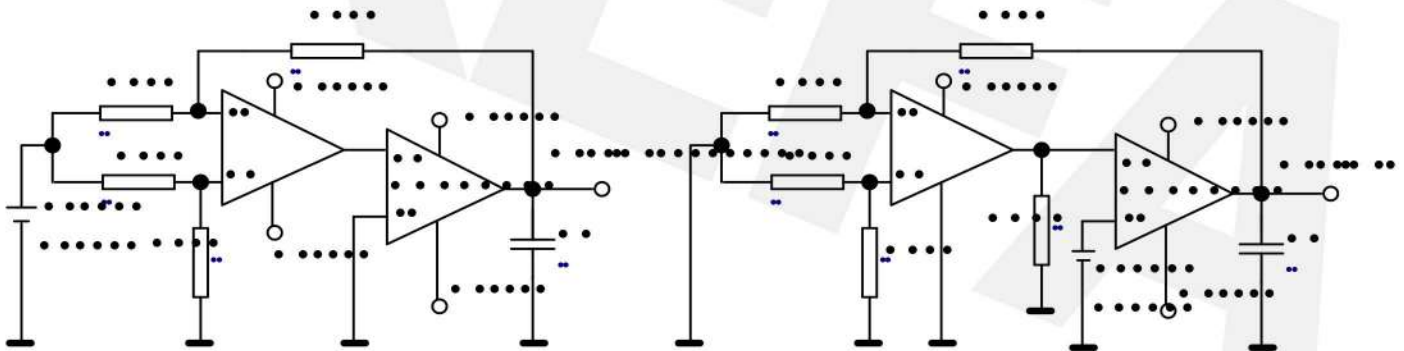


Input Offset Voltage Test Circuit

Input Offset Current Test Circuit

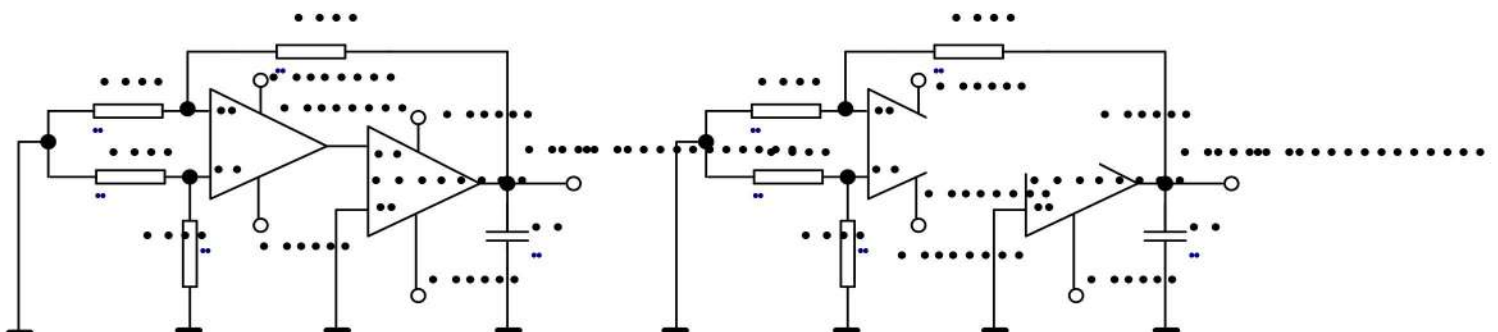


Input Bias Current Test Circuit

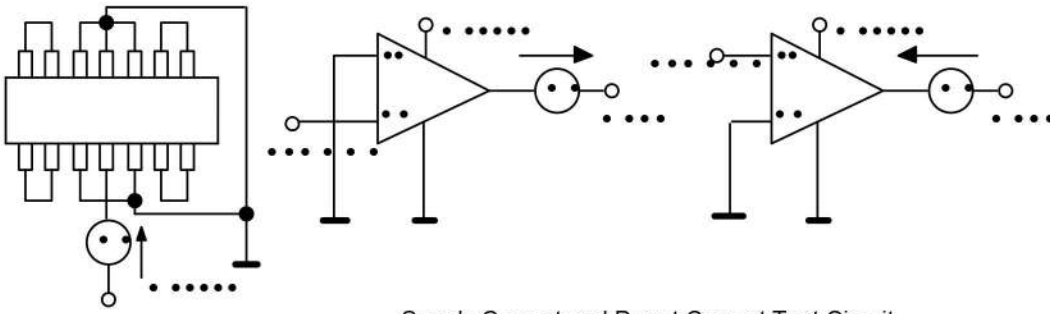


Common-mode Rejection Ratio Test Circuit

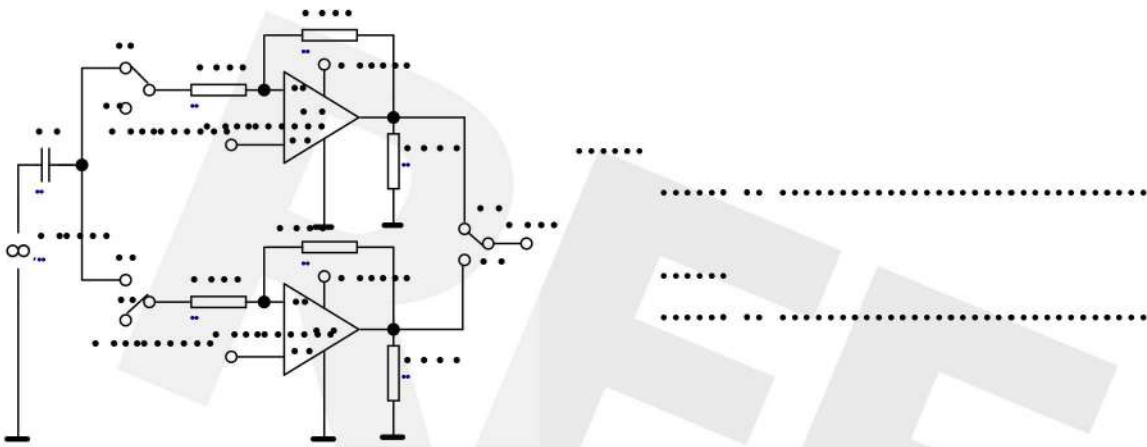
Voltage Gain Test Circuit



Power Supply Rejection Ratio Test Circuit



Supply Current and Duput Current Test Circuit



Channel Separation Test Circuit

